AMENDMENTS TO THE CLAIMS

Please make the following amendments to the claims:

1-32. (Canceled)

33. (Previously Presented) A transmitter, comprising:

a pulse amplitude modulation (PAM) transmitter configured to transmit a point on a signal space constellation, the point representing at least one word;

a trellis encoder associated with the PAM transmitter configured to generate a convolutional code based upon at least one bit of each word;

a fractional encoder associated with the PAM transmitter, the fractional encoder configured to encode a non-integer number of bits for each word to produce an output symbol; and

a constellation encoder configured to map the point on the signal space constellation based upon the output symbol and the convolutional code, the sign of the point being selected by the convolutional code to avoid a bias in positive or negative levels.

- 34. (Previously Presented) The transmitter of claim 33, wherein the fractional encoder is configured to collect an integer number of bits S*K, over a frame comprising several symbol periods S, and convert the S*K bits of the frame into S integers, each of arithmetic base M, where M corresponds to a plurality of PAM signal levels, said PAM signal levels increasing in direct proportion to the values of the S integers.
- 35. (Previously Presented) The transmitter of claim 33, further including a non-fractional encoder associated with the PAM transmitter;

wherein the constellation encoder is configured to map the point based on the same convolutional code generated by the trellis encoder when using the non-fractional encoder while avoiding a bias in the positive or negative level.

- 36. (Previously Presented) The transmitter of claim 33, wherein the trellis encoder generates the convolutional code based upon at least one bit of each word independent of the remaining bits of each word.
- 37. (Previously Presented) The transmitter of claim 33, wherein the at least one bit of each word used to generate the convolutional code bypasses the fractional encoder.
- 38. (Previously Presented) The transmitter of claim 33, wherein the trellis encoder is a feed-forward non-systematic convolutional encoder.
- 39. (Previously Presented) The transmitter of claim 33, further including a PAM encoder table in communication with the constellation encoder;

wherein the constellation encoder is configured to encode the point based upon the PAM encoder table.

40. (Previously Presented) The transmitter of claim 33, further including a fractional encoder table in communication with the fractional encoder:

wherein the fractional encoder is configured to encode the output symbol based on the fractional encoder table.

41. (Currently Amended) A The transmitter of claim 33, comprising:

a pulse amplitude modulation (PAM) transmitter configured to transmit a point on a signal space constellation, the point representing at least one word;

a trellis encoder associated with the PAM transmitter configured to generate a convolutional code based upon at least one bit of each word;

a fractional encoder associated with the PAM transmitter, the fractional encoder configured to encode a non-integer number of bits for each word to produce an output symbol; and

a constellation encoder configured to map the point on the signal space constellation

based upon the output symbol and the convolutional code, such that the mapping maintains the

same convolutional code while avoiding a bias in positive or negative levels,

wherein the signal space constellation comprises N signal levels, the output symbol is represented by C(m), and the level of the point generated by said constellation encoder is selected by the convolutional code from:

-(4C(m)+1)/N or -(4C(m)+3)/N for negative levels;

and from:

(4C(m)+1)/N or (4C(m)+3)/N for positive levels.

- 42. (Cancelled)
- 43. (Previously Presented) The transmitter of claim 33, wherein the fractional encoder is a modulus converter.
- 44. (Previously Presented) The transmitter of claim 33, wherein the fractional encoder is configured to collect an integer number of bits S*K, over a frame comprising several symbol periods S, and is configured to encode the frame of S*K bits for transmission at a fractional bit rate of K bits per symbol.

45. (Previously Presented) The transmitter of claim 44, wherein the fractional encoder is configured to convert the S*K bits of the frame into S integers, each of arithmetic base M, where M corresponds to a plurality of PAM signal levels.

46. (Previously Presented) A method for encoding fractional bit rates using pulse amplitude modulation (PAM), the method comprising the steps of:

using a PAM modulator to generate a point on an signal space constellation, the point representing at least one word;

generating a convolutional code based upon at least one bit of each word;

encoding a non-integer number of bits for each word to produce an output symbol; and
mapping the point on the signal space constellation based upon the output symbol and the
convolutional code, the sign of the point being selected by the convolutional code to avoid a bias
in positive or negative levels.

- 47. (Previously Presented) The method of claim 46, further including: collecting an integer number of bits S*K, over a frame comprising several symbol periods S, and converting the S*K bits of the frame into S integers, each of arithmetic base M, where M corresponds to a plurality of PAM signal levels, said PAM signal levels increasing in direct proportion to the values of the S integers.
- 48. (Previously Presented) The method of claim 46, further including:

 mapping the point based on the same convolutional code generated by the trellis encoder

 when using a non-fractional encoder, while avoiding a bias in the positive or negative level.

49. (Previously Presented) The method of claim 46, wherein the step of generating a convolutional code based upon at least one bit of each word further comprises generating the convolutional code independent of the remaining bits of each word.

- 50. (Previously Presented) The method of claim 46, wherein the step of mapping the point on the signal space constellation further includes encoding the point based upon a PAM encoder table.
- 51. (Previously Presented) The method of claim 46, wherein the step of encoding a non-integer number of bits for each word to produce the output symbol further includes encoding the output symbol based on a fractional encoder table.
- 52. (Previously Presented) The method of claim 46, wherein the signal space constellation comprises N signal levels, the output symbol is represented by C(m), and the step of mapping the point on the signal space constellation further includes:

generating the level of the point, the level selected by the convolutional code from:

-(4C(m)+1)/N or -(4C(m)+3)/N for negative levels;

and from:

(4C(m)+1)/N or (4C(m)+3)/N for positive levels.

- 53. (Previously Presented) The method of claim 46, further comprising: decoding a received symbol into a non-integer number of bits.
- 54. (Previously Presented) The method of claim 46, wherein the encoding step includes modulus conversion.

55. (Previously Presented) The method of claim 46, further comprising the steps of: collecting an integer number of bits S*K, over a frame comprising several symbol periods S; and

encoding the frame of S*K bits for transmission at a fractional bit rate of K bits per symbol.

56. (Previously Presented) The method of claim 55, further comprising the step of converting the S*K bits of the frame into S integers, each of arithmetic base M, where M corresponds to a plurality of PAM signal levels.

57-65. (Cancelled)

66. (New) A transmitter, comprising:

a pulse amplitude modulation (PAM) transmitter configured to transmit a point on a signal space constellation, the point representing at least one word;

a trellis encoder associated with the PAM transmitter configured to generate a convolutional code based upon at least one bit of each word;

a fractional encoder associated with the PAM transmitter, the fractional encoder configured to encode a non-integer number of bits for each word to produce an output symbol; and

a constellation encoder configured to map the point on the signal space constellation based upon the output symbol and the convolutional code, the sign of the point being selected by the convolutional code.

67. (New) The transmitter of claim 66, wherein the signal space constellation comprises N signal levels, the output symbol is represented by C(m), and the level of the point is

selected by the convolutional code from -(4C(m)+1)/N or -(4C(m)+3)/N for negative levels, and from +(4C(m)+1)/N or (4C(m)+3)/N for positive levels.

68. (New) The transmitter of claim 66 further including a non-fractional encoder associated with the PAM transmitter;

wherein the constellation encoder is configured to map the point based on the same convolutional code generated by the trellis encoder when using the non-fractional encoder while avoiding a bias in the positive or negative level.

- 69. (New) The transmitter of claim 66, wherein the fractional encoder is configured to collect an integer number of bits S*K, over a frame comprising several symbol periods S, and is configured to encode the frame of S*K bits for transmission at a fractional bit rate of K bits per symbol.
- 70. (New) The transmitter of claim 66, wherein the trellis encoder generates the convolutional code based upon at least one bit of each word independent of the remaining bits of each word.
- 71. (New) The transmitter of claim 66, wherein the at least one bit of each word used to generate the convolutional code bypasses the fractional encoder.
- 72. (New) The transmitter of claim 66, wherein the trellis encoder is a feed-forward non-systematic convolutional encoder.
- 73. (New) The transmitter of claim 66, further including a PAM encoder table in communication with the constellation encoder;

wherein the constellation encoder is configured to encode the point based upon the PAM encoder table.

74. (New) The transmitter of claim 66, further including a fractional encoder table in communication with the fractional encoder;

wherein the fractional encoder is configured to encode the output symbol based on the fractional encoder table.

75. (New) The transmitter of claim 66, wherein the fractional encoder is a modulus converter.